

/ Descriptions

WR0553 QSQ Silicon NPN transistor in a TO-220 Plastic Package.

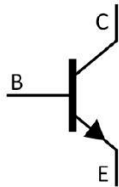
/ Features

F0E $Y_{FER@483Y}$ $I_F@8D$
High Collector-Base Voltage, High Collector Current.

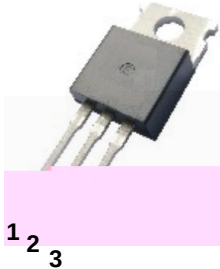
/ Applications

B/W TV horizontal deflection output.

/ Equivalent Circuit



/ Pinning



PIN1 Base PIN 2 Collector PIN 3 Emitter

/ h_{FE} Classifications & Marking

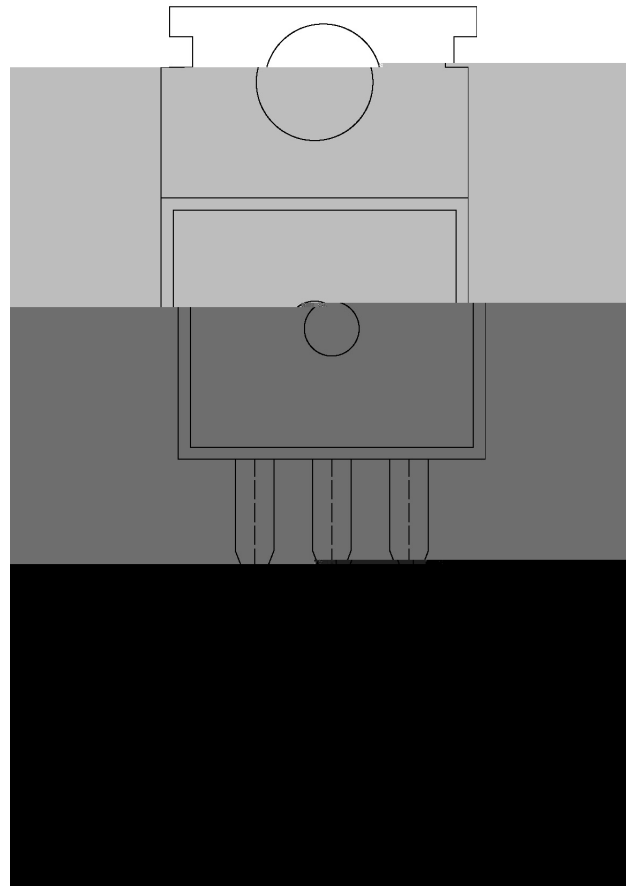
h_{FE} Classifications Symbol	N	R	O
h_{FE} Range	20 50	40 80	70 140

/ Electrical Characteristic Curve

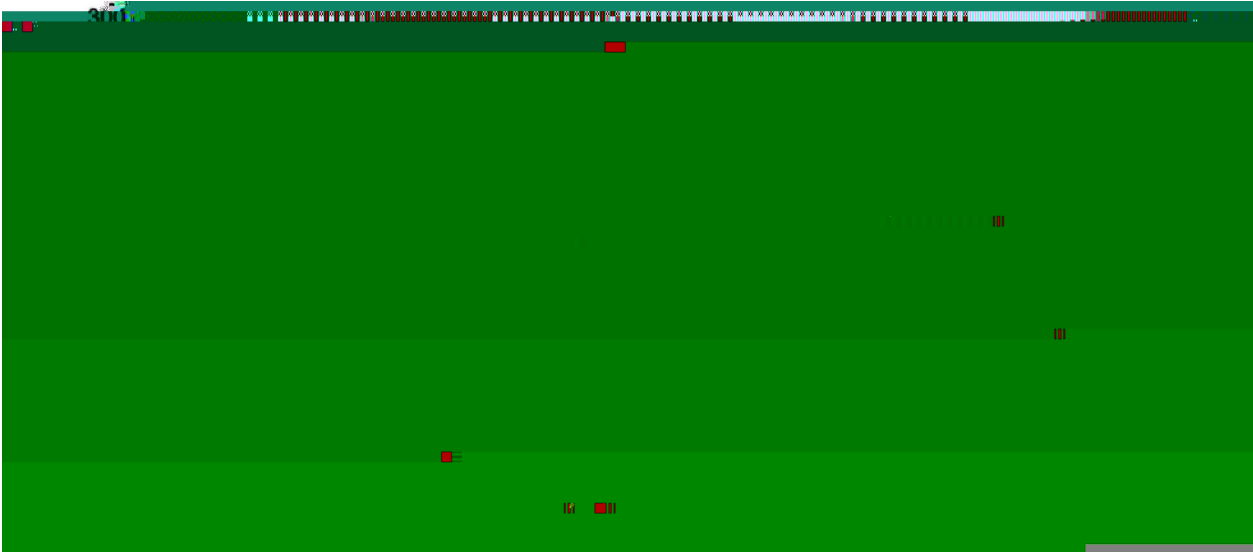
/ Package Dimensions



/ Marking Instructions



() / Temperature Profile for Dip Soldering(Pb-Free)



- Note:
- 1.Preheating:25~150%, Time:60~90sec.
 - 2.Peak Temp.:255[±]5%, Duration:5[±]0.5sec.
 - 3. Cooling Speed: 2~10%/sec.

/ Resistance to Soldering Heat Test Conditions

270[±]5% 10[±]1 sec. Temp.:270±5 Time:10±1 sec

/ Packaging SPEC.

BULK

Package Type 封装形式	Units 包					Dimension 包尺寸 (nit: mm ³)		
	Units/Bag 只/袋	Bags/Inner Box 袋/套	Units/Inner Box 只/套	Inner Boxes/Outer Box 套/箱	Units/Outer Box 只/箱	Bag 袋	Inner Box 套	Outer Box 箱
TO-220/F	200	10	2,000	5	10,000	135×190	237×172×102	560×245×195

TUBE

Package Type 封装形式	Units 包					Dimension 包尺寸 (nit: mm ³)		
	Units/Tube 只/套	Tubes/Inner Box 套/套	Units/Inner Box 只/套	Inner Boxes/Outer Box 套/箱	Units/Outer Box 只/箱	Tube 套	Inner Box 套	Outer Box 箱
TO-220/F								