BRMJD32CQ

Rev.A Apr.-2024

DATA SHEET

/ Descriptions

WR0585 ṂṂ Ŀ SQS ӕṀ ӌ s Ⅱ ᄀ Silicon PNP transistor in a TO-252 Plastic Package.

/ Features

Ḩ ﻫ Ȁ ҥ ı ق ڒ ẻ ᾰ ˊ ˛ ˚ Ŀ Ẕ ፣ ı Ⴁ ᵀ DHF0T434 ‰ˊ ػ ˡ ˛ ớ ɭ ∂ ı ῇ ғ ᵝ

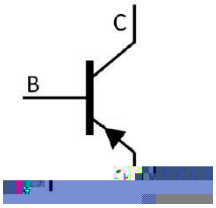
Low Speed ,Load Formed for Surface Mount Application, Qualified to AEC-Q101 Standards for High Reliability, HF Product.

/ Applications

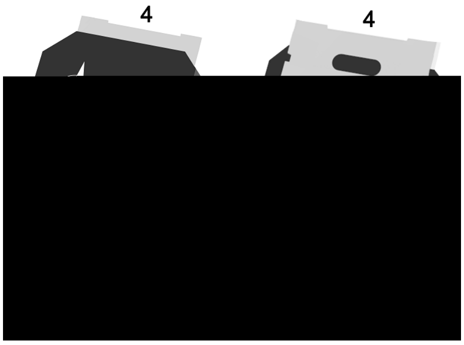
ٻ ፣ ῷ ᶻ ᴴ ı ◗ ʼ ∩ ج Ẕ ፣ Є ɭ ∂

General Purpose Amplifier, Meet the stringent requirements of automotive applications.

/ Equivalent Circuit



/ Pinning



PIN1 χ Base PIN 2,4 χ Collector PIN 3 χ Emitter

/ h_{FE} Classifications & Marking

ȯ ᴍ Ⴁ ب ˝

See Marking Instructions.

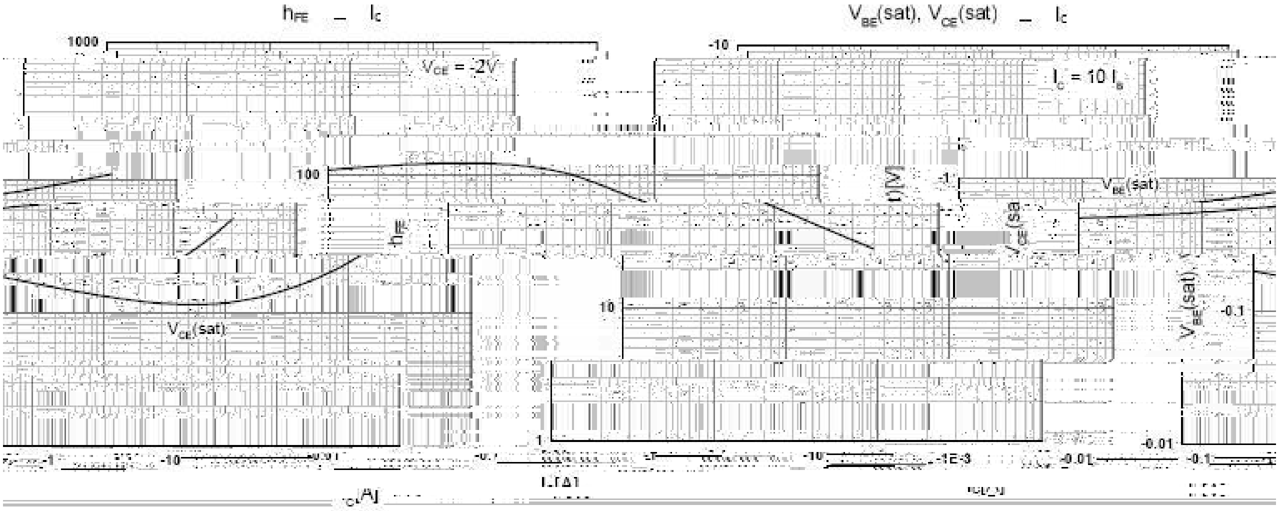
v ῼ̋ Parameter	ɣ ᴺ Symbol	ῼ̋Ӫ Rating	ᴅ Ʉ Unit
Collector to Base Voltage	V_{CBO}	-100	V
Collector to Emitter Voltage	V_{CEO}	-100	V
Emitter to Base Voltage	V_{EBO}	-5.0	V

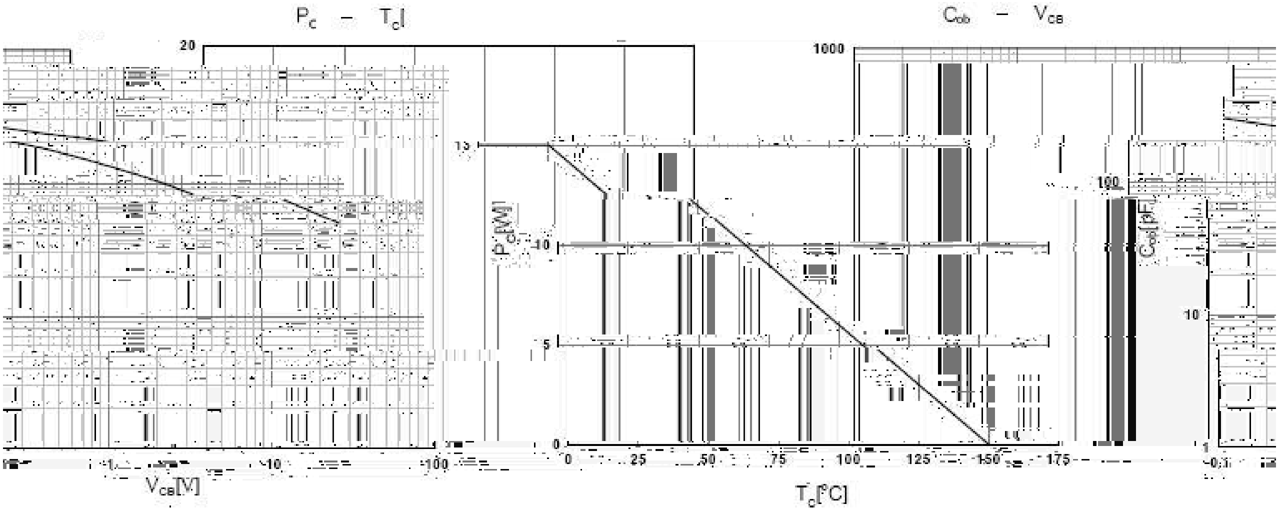
[redacted]

[redacted]

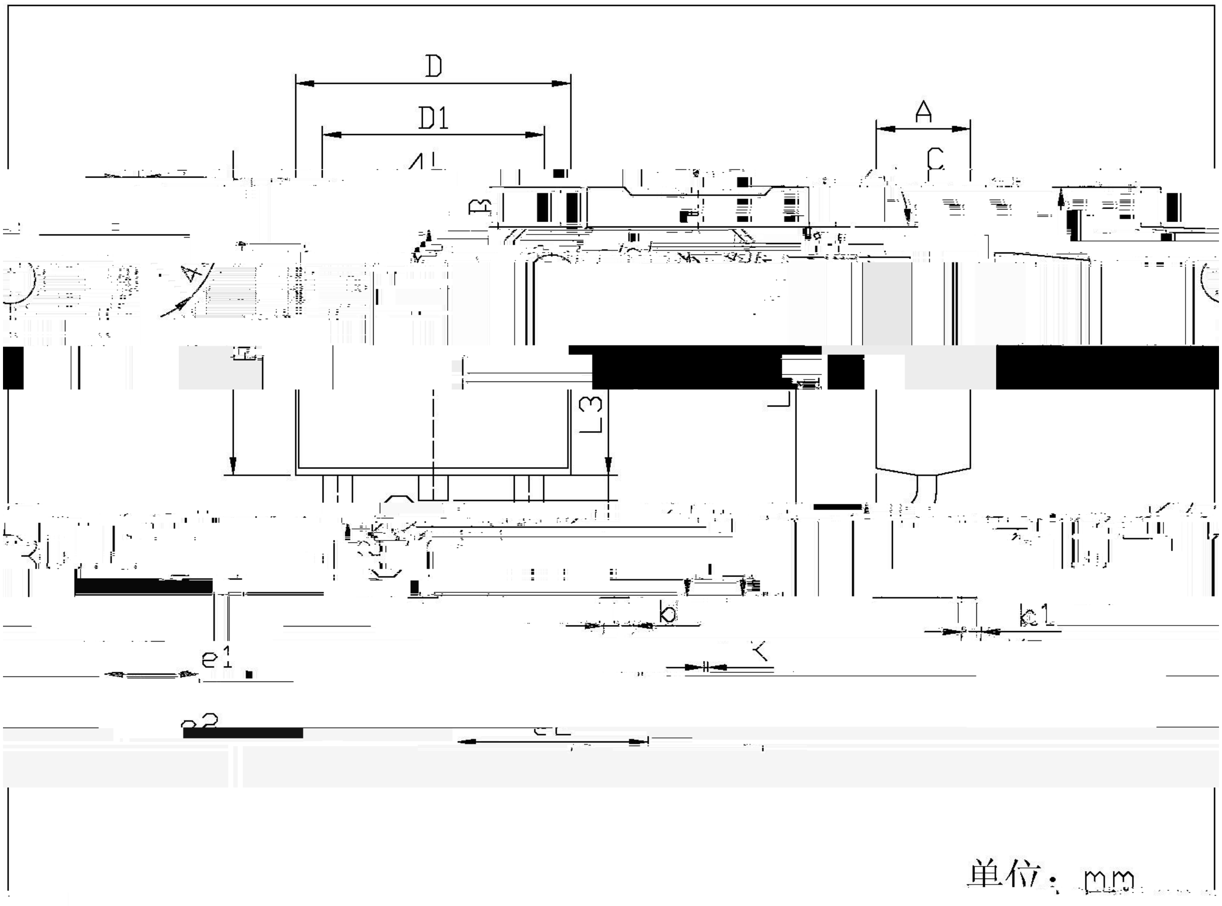
[redacted]

/ Electrical Characteristic Curve





/ Package Dimensions

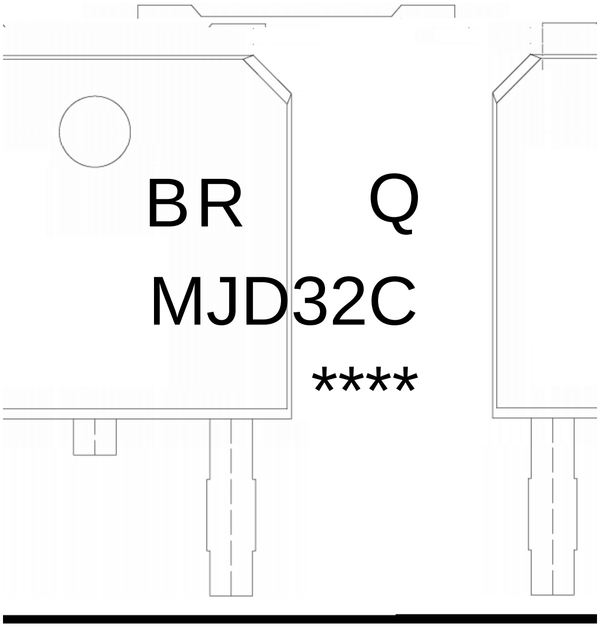


单位：mm

Symbol	Min	Max	Symbol	Min	Max	Symbol	Min	Max
A	[illegible]	2.40	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]
B	0.95	1.25	e1	2.24	2.34	[illegible]	[illegible]	[illegible]
b	0.70	0.90	e2	4.43	4.73	[illegible]	[illegible]	[illegible]
b1	0.45	0.55	[illegible]	[illegible]	0.85	[illegible]	[illegible]	10.35
C	0.45	0.55	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]
D	6.45	6.75	[illegible]	[illegible]	0.60	[illegible]	[illegible]	[illegible]
D1	5.10	5.50	K	[illegible]	[illegible]	[illegible]	0.00	0.10

TO-252

/ Marking Instructions



ب ˝ χ

BR χ	Ѭ ʇ ᴎ Ɫƀ
Q χ	Ѭ ∩ ج ῃ ғ ᵝ ‰ ∜
P〷G65F χ	Ѭ ғ ᵝ ʒ ᴺ
---- χ	Ѭ ꞁ ғ ῃ ᴺ Ɫƀ ι ҿ ꞁ ғ ῃ ᴺ ψ ק

Note:

BR:	Company Code
Q:	Automobile halogen-free product Code
MJD32C:	Product Type
****:	Lot No. Code, code change with Lot No

( ) / Temperature Profile for IR Reflow Soldering(Pb-Free)

ب ˝ χ

1 ɩ̉ ♬ □ ẙ 150 Ḱ 200 ł ı ̀H ڗ 60 Ḱ 120sec;

2 Ś̈ Ӫ □ ẙ 255 ° 5 ł ı ̀H ڗ ʿY ʎ̸ ʭ 5 ° 0.5sec;

3 ì ˙ ˥ ˛ ɔ ه ẙ ʭ 2 Ḱ 10 ł /sec.

Note:

1.Preheating:150~200 ł , Time:60~120sec.

2.Peak Temp.:255 ° 5 ł , Duration:5 ° 0.5sec.

3. Cooling Speed: 2~10 ł /sec.