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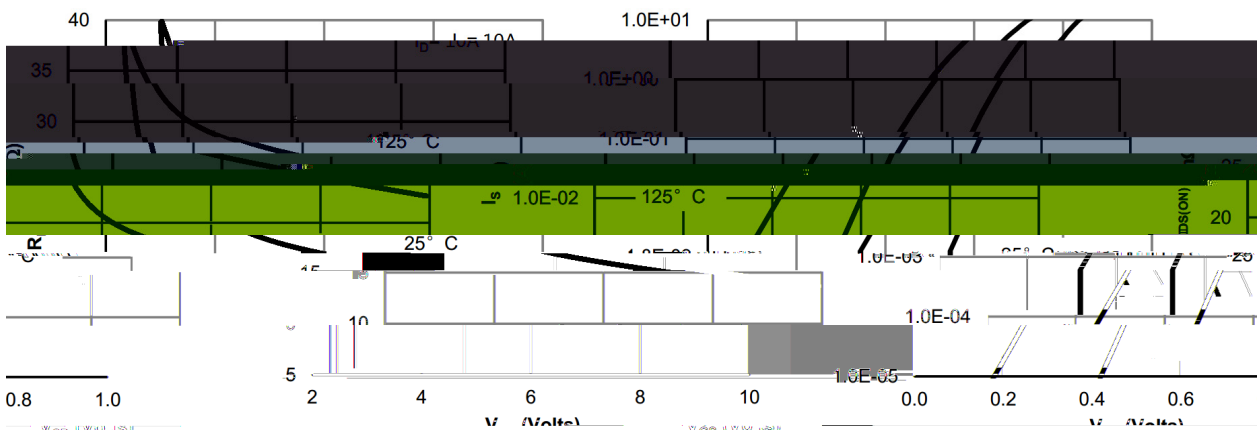
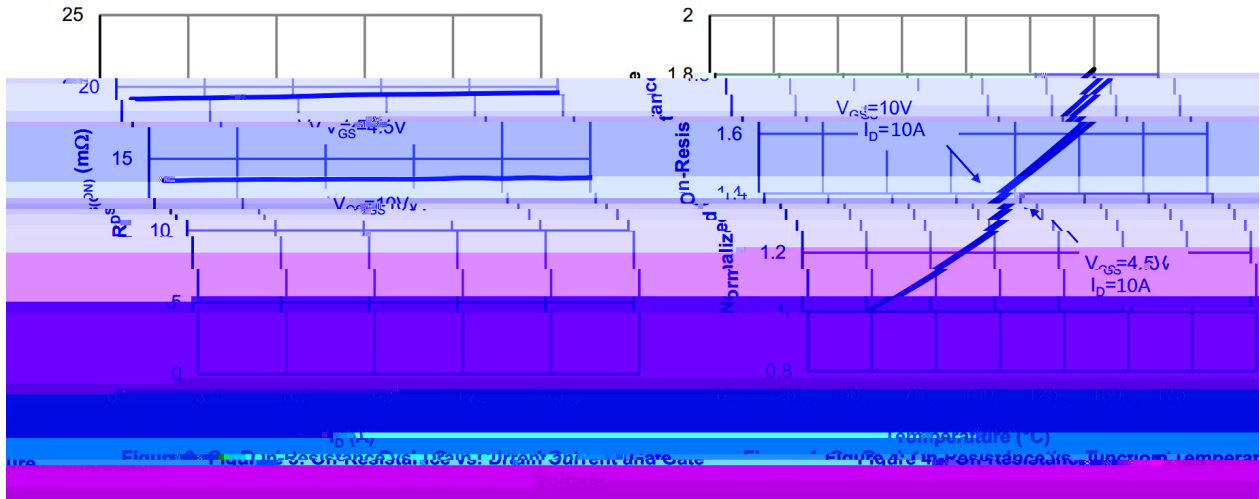
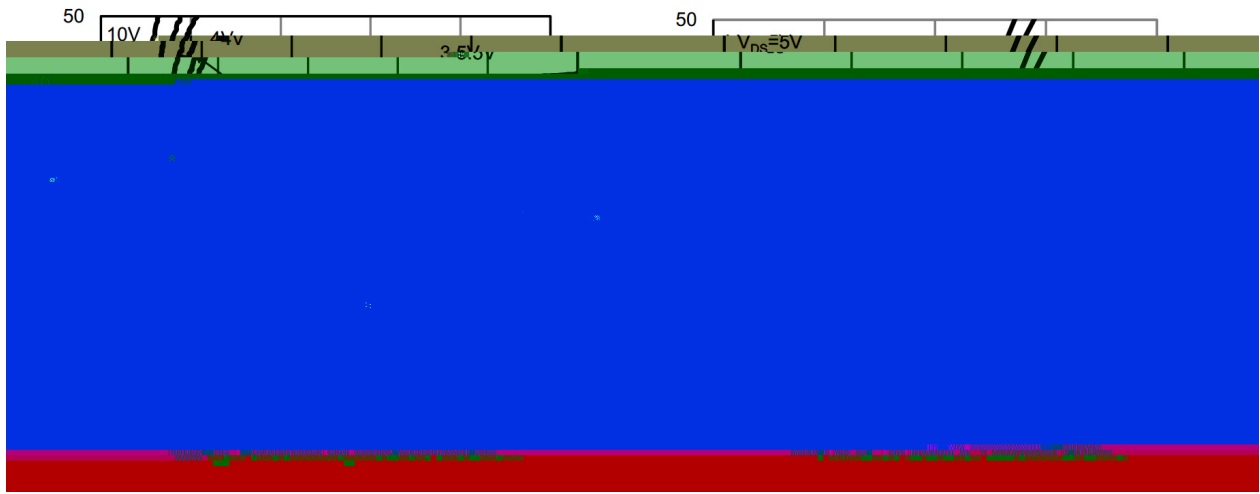
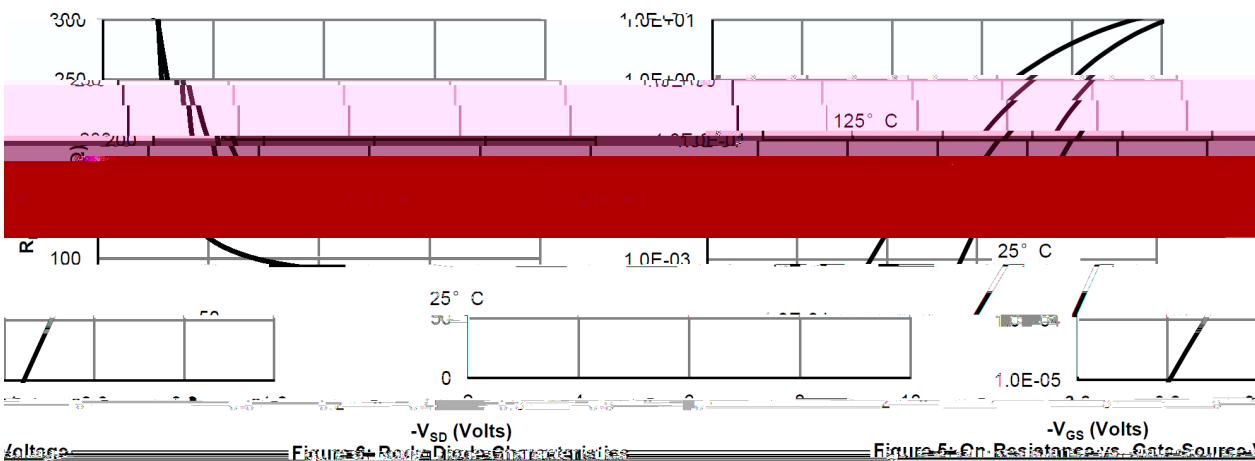
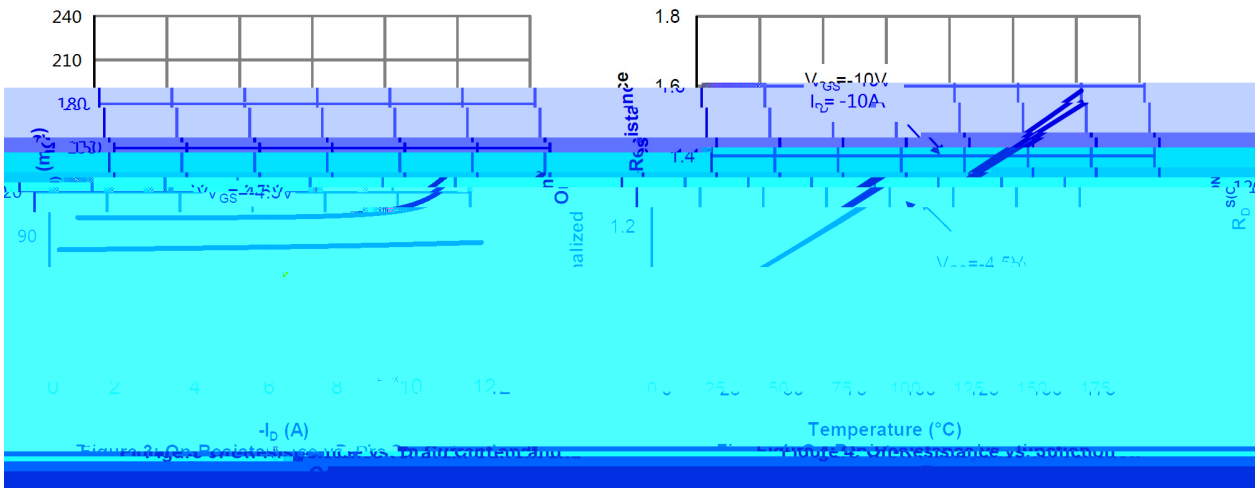
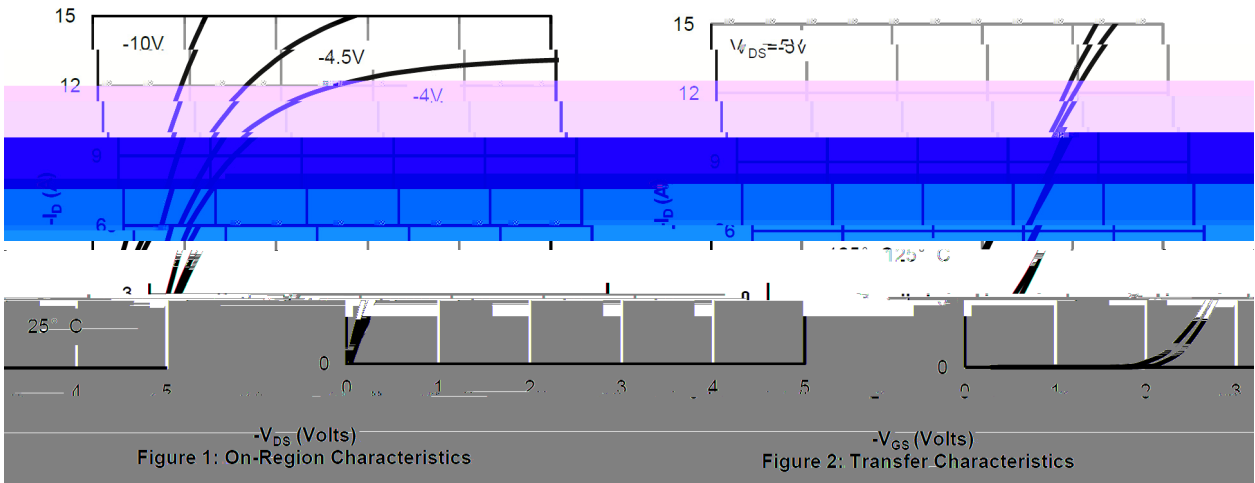


Figure 5: On-Resistance vs. Gate-Source Voltage

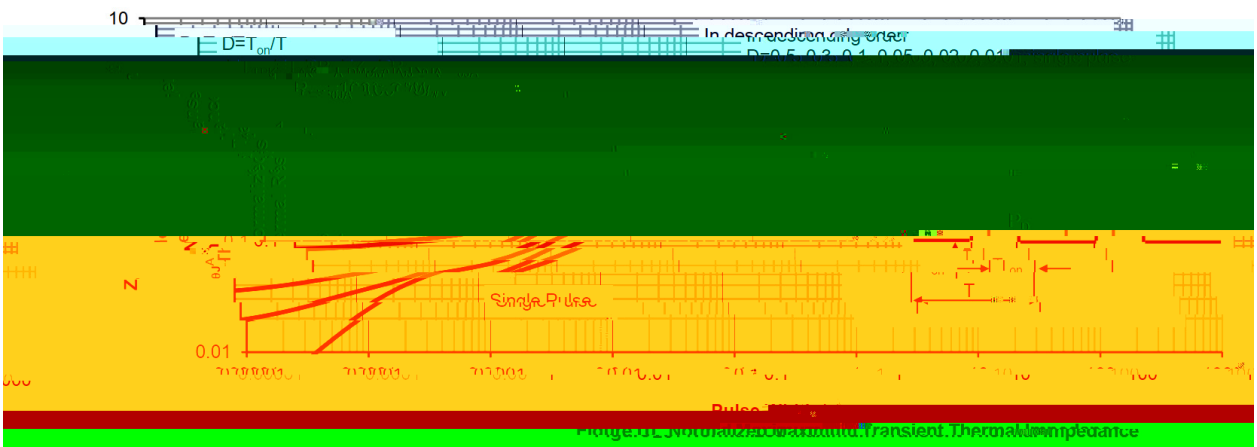
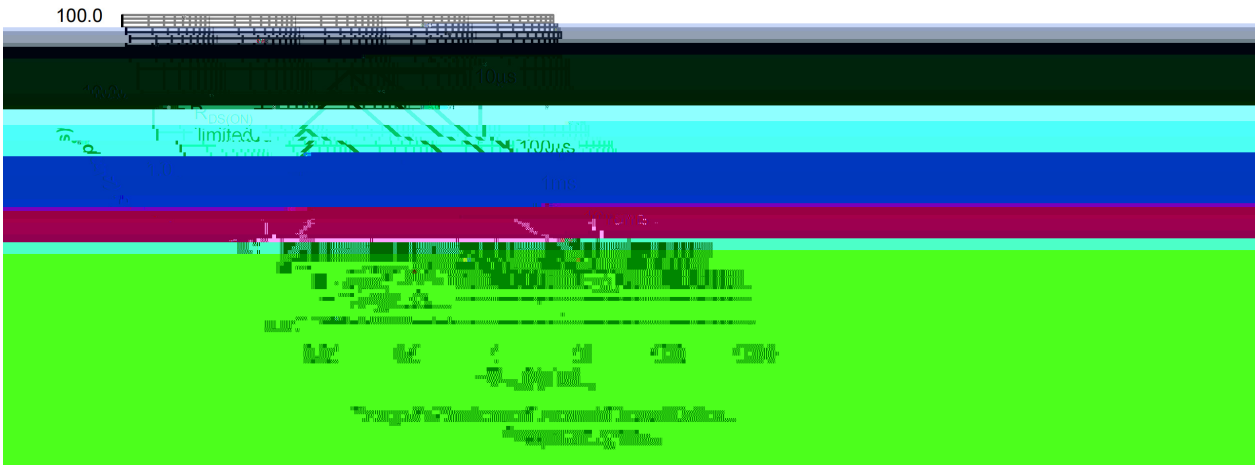
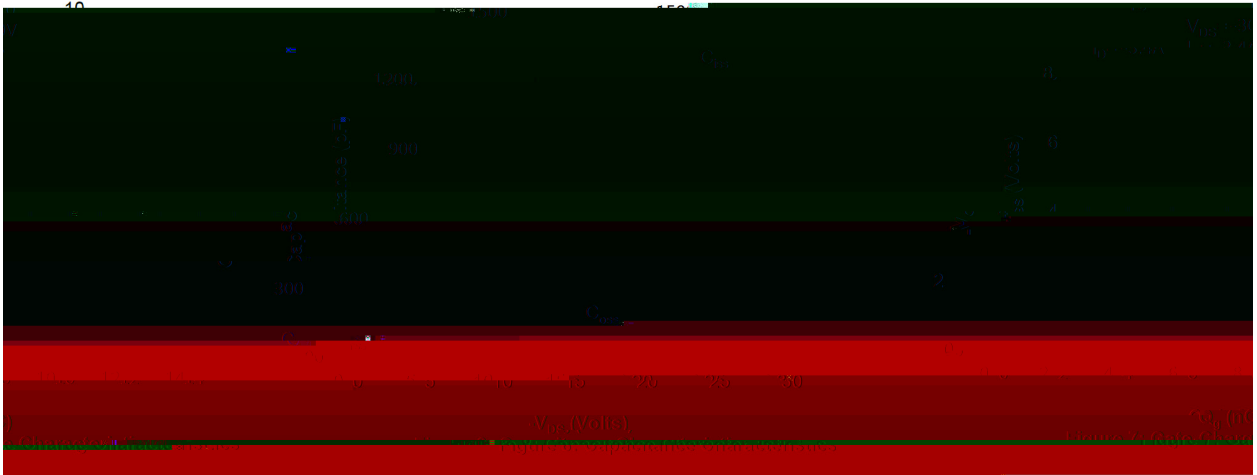
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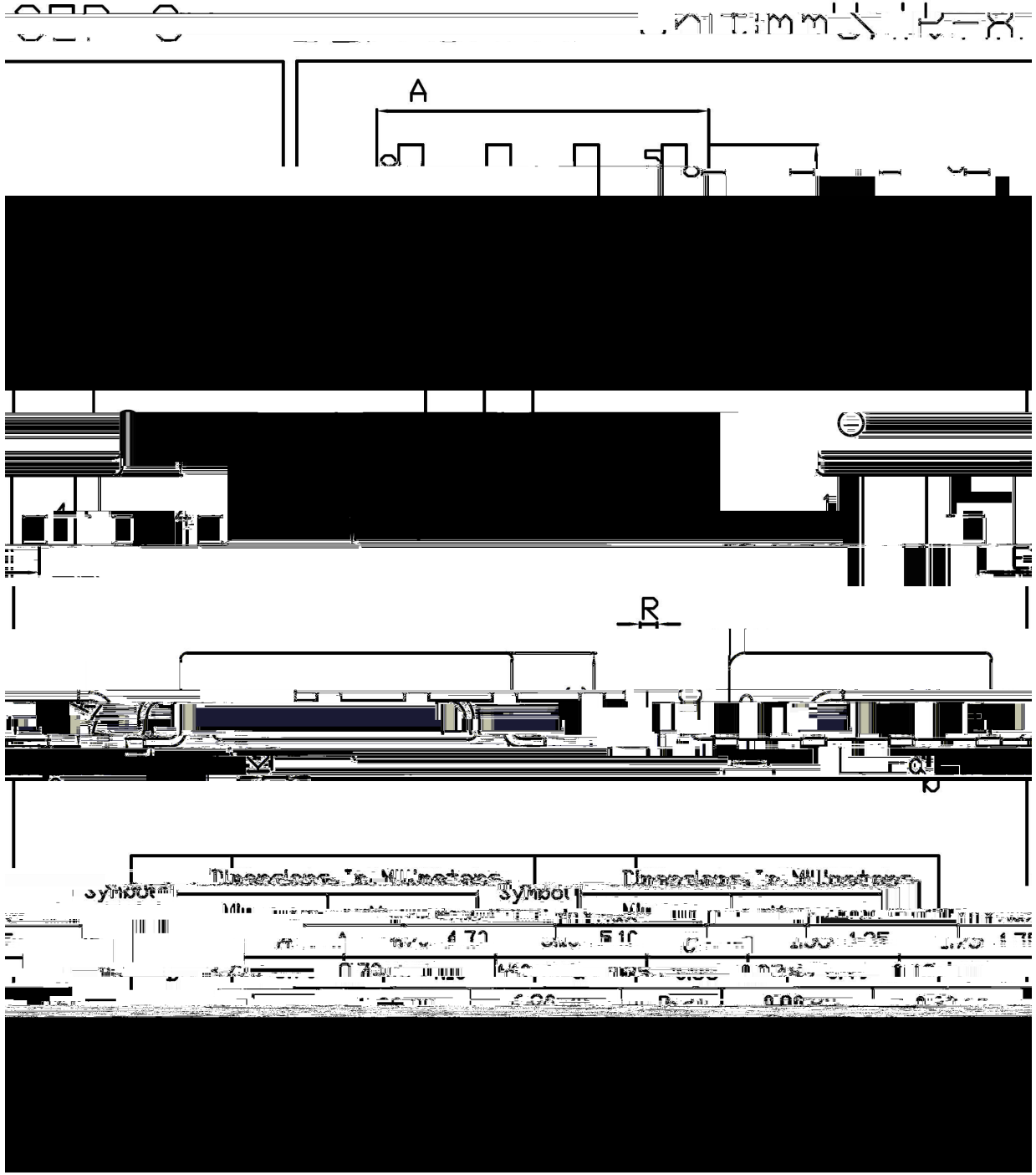
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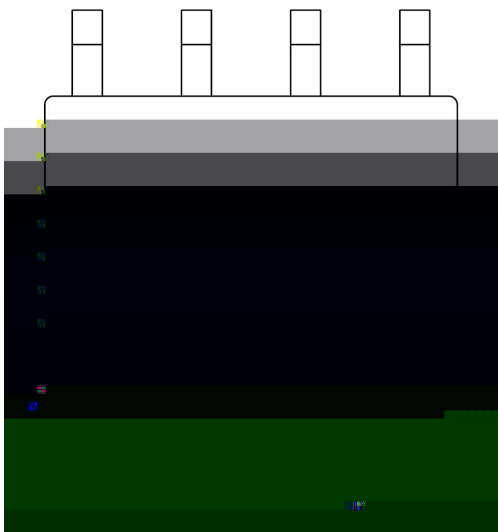
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