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N-Channel Enhancement Mode Field Effect Transistor in a DFN1006-3L Plastic Package.

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Sensitive gate trigger current and Low Holding current.ESD protected up to 2KV,HF Product.

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/TZKTJKJ LUX [YK OT MKTKXGR V[XVUYK Y]GZVROTCMZG UJ WNGYK IUTZ

Pin1:G Pin2:S Pin3:D

@ f Parameter		... Z Symbol	f › Rating	% y Unit
Drain-Source Voltage		V _{bss}	20	V
Gate-Source Voltage		V _{GSS}	r8	V
Drain Current – Continuous		I _D	0.75	A
Pulsed Drain Current		I _{DM}	1.8	A
Power Dissipation		P _D	0.9	W
Storage Temperature Range		T _{stg}	-55 to 150	-
Thermal resistance, junction - ambient	t 0 10s	R _{JA}	100	- /W
	Steady-State		140	

Electrical Characteristics(Ta=25 ;)

@ f Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Total Gate Charge	Q_g	$V_{GS}=4.5V,$ $I_D=0.4A$ $V_{DS}=10V,$		0.85		nC
Gate Source Charge	Q_{gs}			0.1		
Gate Drain Charge	Q_{gd}			0.25		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=4.5V$ $R_L=25$ $V_{DS}=10V$ $R_{GEN}=3$		2		ns
Turn-On Rise Time	t_r			4		ns
Turn-Off Delay Time	$t_{d(off)}$			18		ns
Turn-Off Fall Time	t_f			8		ns

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Ô ? d • Ž ¢ / Electrical Characteristic Curve

Ø □ =) ϕ / Package Dimensions

, M y f / Marking Instructions

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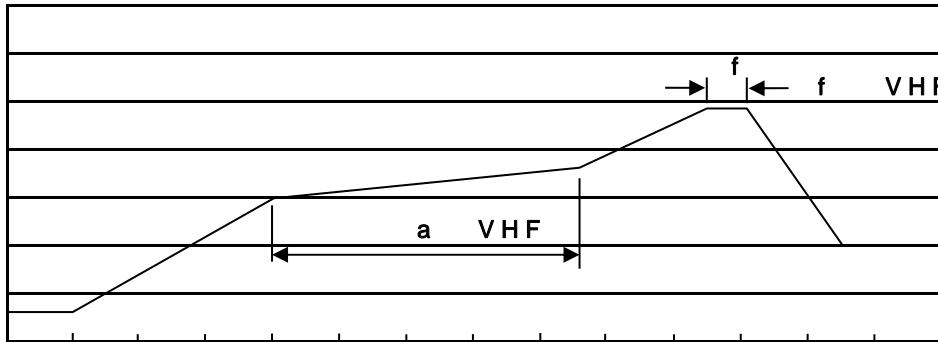
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šWD t...•Žϕ (x/) / :KSVKXGZ[XK 6XULORK LUX /8 8KLRU] 9URJKXOTM 6

7HPSHUDWXUH



7LPH VHF

a ϕ y

1o• Ä ½ “ † 150 ½180 - k ž • 60 ½90sec;

2o• Q › “ † 245 r5 - k ž • 4 Ò 5 r0.5sec;

3o•D N ò i Ò 0 , † 2 ½10 - /sec.

Note:

1.Preheating:150~180 - , Time:60~90sec.

2.Peak Temp.:245 r5 - , Duration:5 r0.5sec.

3. Cooling Speed: 2~10 - /sec.

ÂD /Cã p ~ »] / Resistance to Soldering Heat Test Conditions

“ † y 260 r5 -

ž • y 10 r1 sec.

Temp.:260±5

Time:10±1 sec

G P á / Packaging SPEC.

2 & x / REEL

Package Type 7>û ~ E	Units ;>û !H					Dimension ;>û p . (unit Åmm³)		
	Units/Reel /--	Reels/Inner Box -- /-	Units/Inner Box /-	Inner Boxes/Outer Box - /!ç	Units/Outer Box /!ç	Reel	Inner Box	Outer Boxç
DFN1006-3L	10,000	10	100,000	6	600,000	7 s x8	180x120x180	390x385x205

„Đ y f / Notices