

/ Descriptions

KF 50) CD GE G` Silicon PNP transistor in a TO-92LM Plastic Package.

/ aθdcθatures

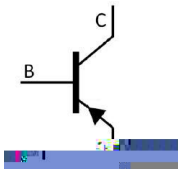
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Excellent high current linearity, low saturation voltage.

/ aApplicationsaθdc0

High frequency low power amplifier applications.

/ aθdcθEquivalent Circuit



/ 5 d c 0 Pinning



PIN1 Base PIN 2 Collector PIN 3 Emitter

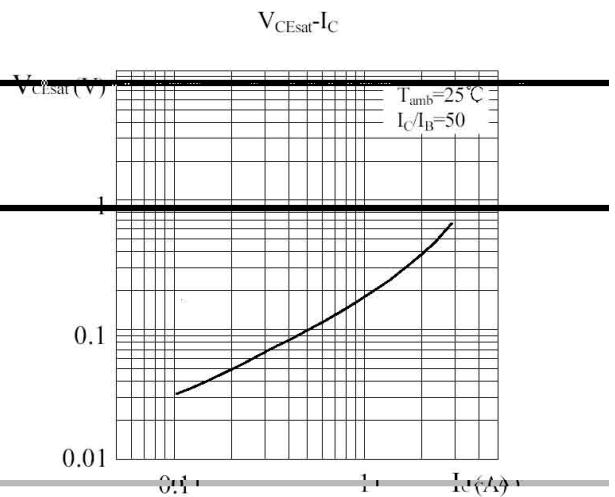
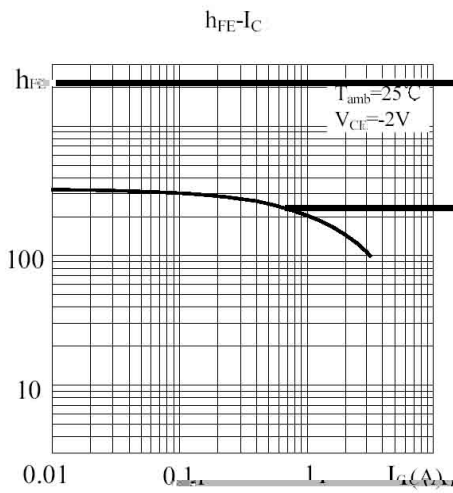
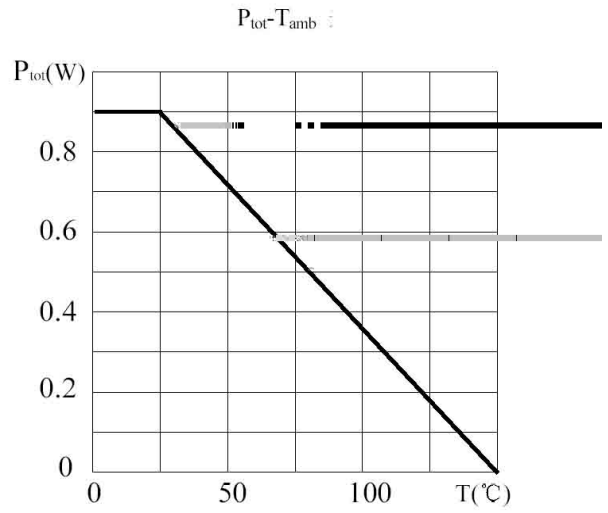
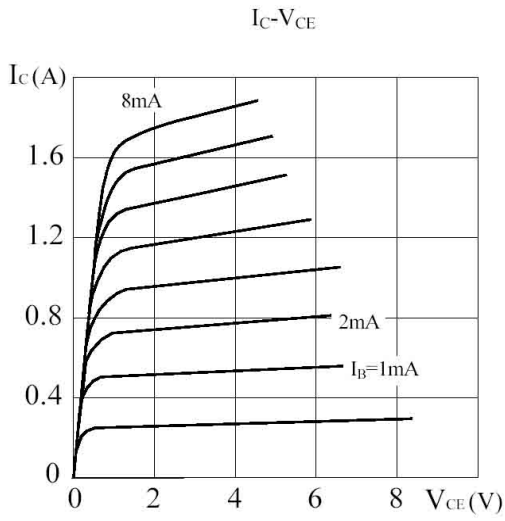
/ h_{FE} Classifications & Marking

h_{FE}

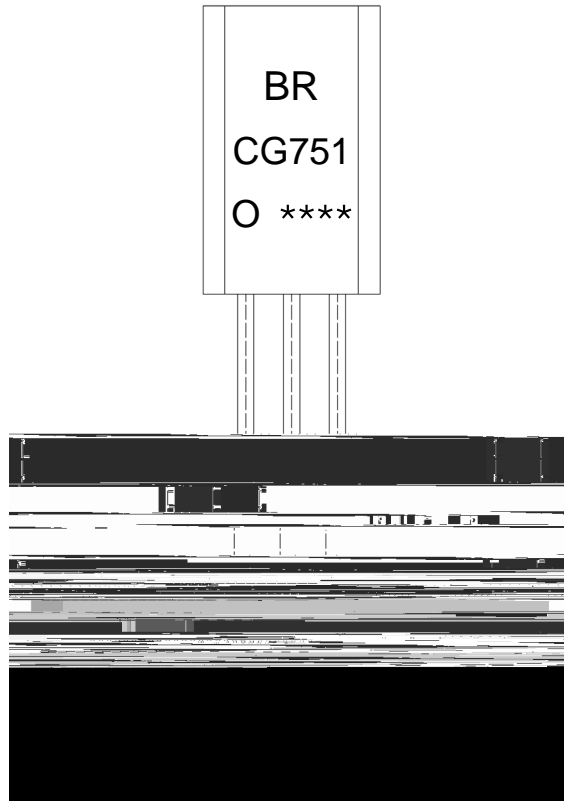
Parameter

Symbol

/ Electrical Characteristic Curve



/ Marking Instructions



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Note:

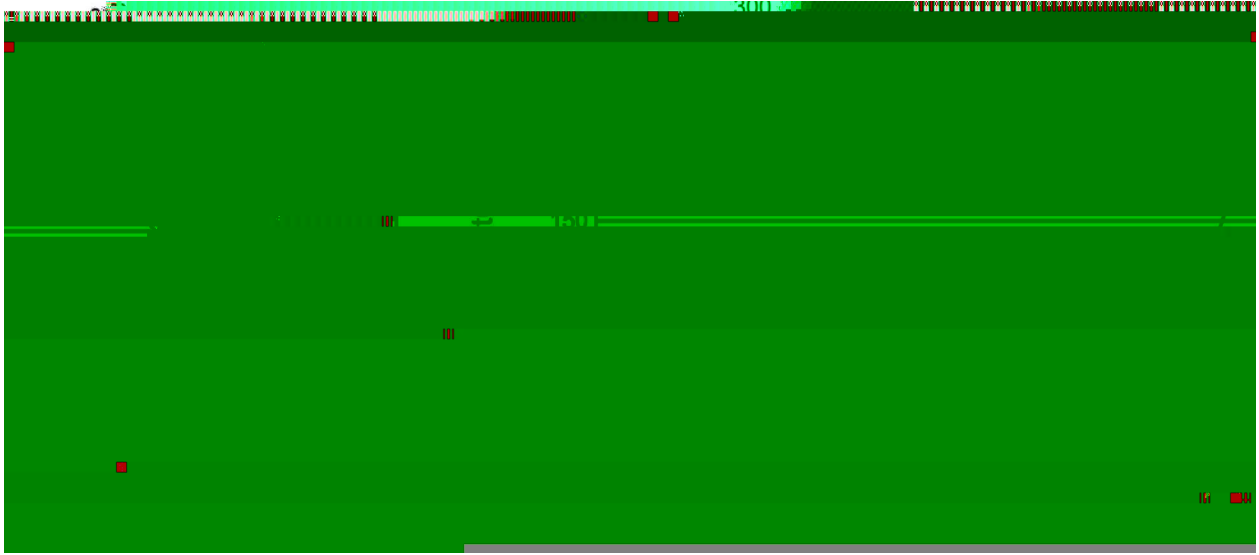
BR: Company Code.

CG751: Product Type.

O: h_{FE} Classifications Symbol

****: Lot No. Code, code change with Lot No.

() / Temperature Profile for Dip Soldering(Pb-Free)



<p>1 25 150 60 90sec;</p> <p>2 255..5 5..0.5sec;</p> <p>3 2 10 /sec.</p>	<p>Note:</p> <p>1.Preheating:25~150 , Time:60~90sec.</p> <p>2.Peak Temp.:255..5 , Duration:5..0.5sec.</p> <p>3. Cooling Speed: 2~10 /sec.</p>
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/ Resistance to Soldering Heat Test Conditions

270..5 10..1 sec. Temp.:270±5 Time:10±1 sec

/ Packaging SPEC.

/ BULK

Package Type	Units					Dimension			(unit mm3)
	Units/Bag	Bags/Inner Box	Units/Inner Box	Inner Boxes/Outer Box	Units/Outer Box	Bag	Ø)	Ø)	
	/	/	/	/	/				