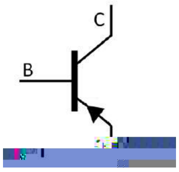
WR0<50P ṂṂ Ŀ SQS ӔṀ ɥ s ǁ ʼ1 Silicon PNP transistor in a TO-92LM Plastic Package.

љ 5VG99 : +D, Й ´
Complementary pair with 2SD667(A).

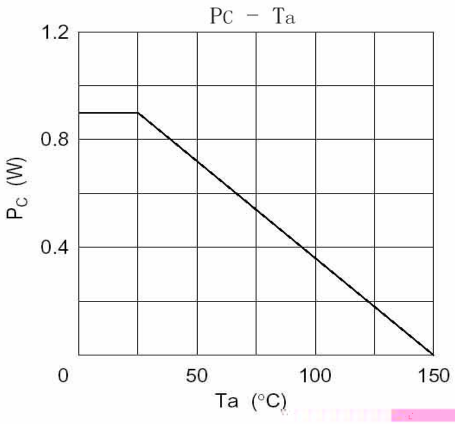
Ȑ Ḩ ḭ ı ' ὢ ᶻ
Low frequency power amplifier.

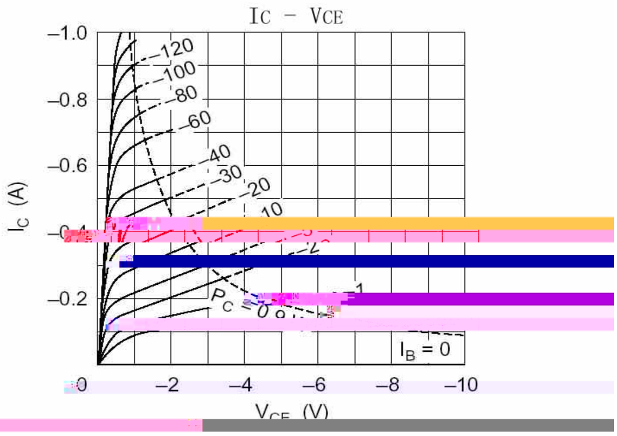


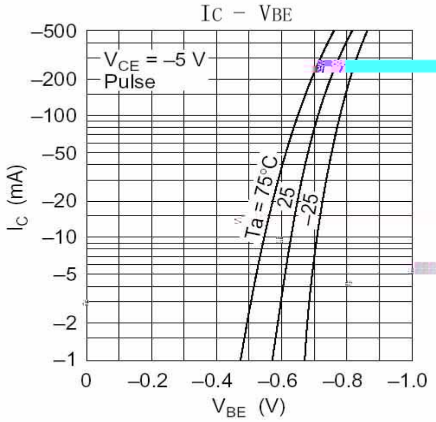
PIN1 χ Base PIN 2 χ Collector PIN 3 χ Emitter

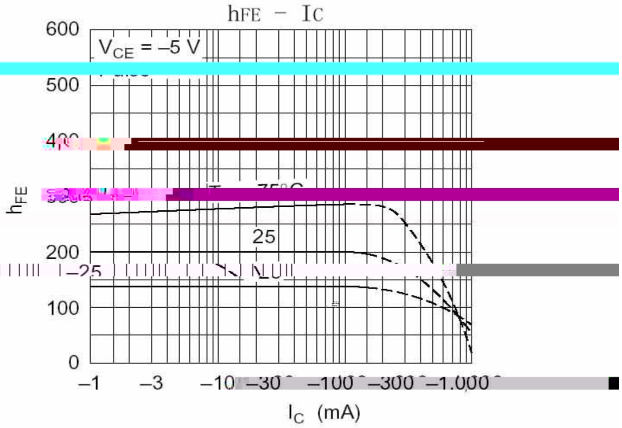
v ‶Ω̣ Parameter	ˀ ᴺ Symbol		‶Ω̣Ӫ Rating	D Ꞓ Unit
Collector to Base Voltage	V_{CBO}		-120	V
Collector to Emitter Voltage	V_{CEO}	2SB647	-80	V
		2SB647A	-100	
Emitter to Base Voltage	V_{EBO}		-5.0	V
Collector Current (DC)	I_C		-1.0	A
Collector Current(Pulse)	I_{CP}		-2.0	A
Collector Power Dissipation	P_C		900	mW
Junction Temperature	T_j		150	
Storage Temperature Range	T			

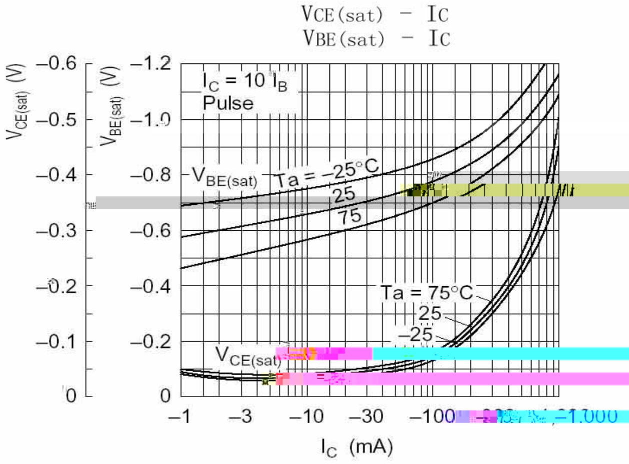
/ Electrical Characteristic Curve

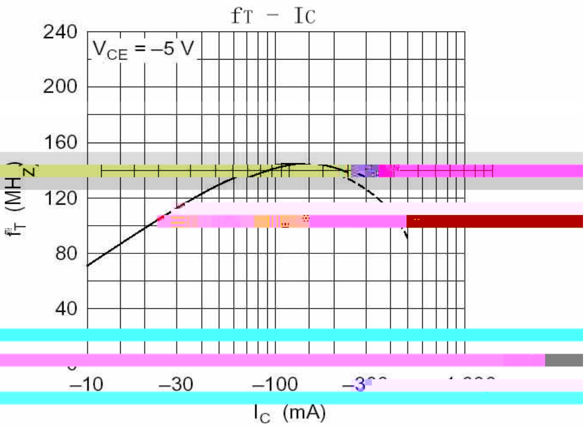








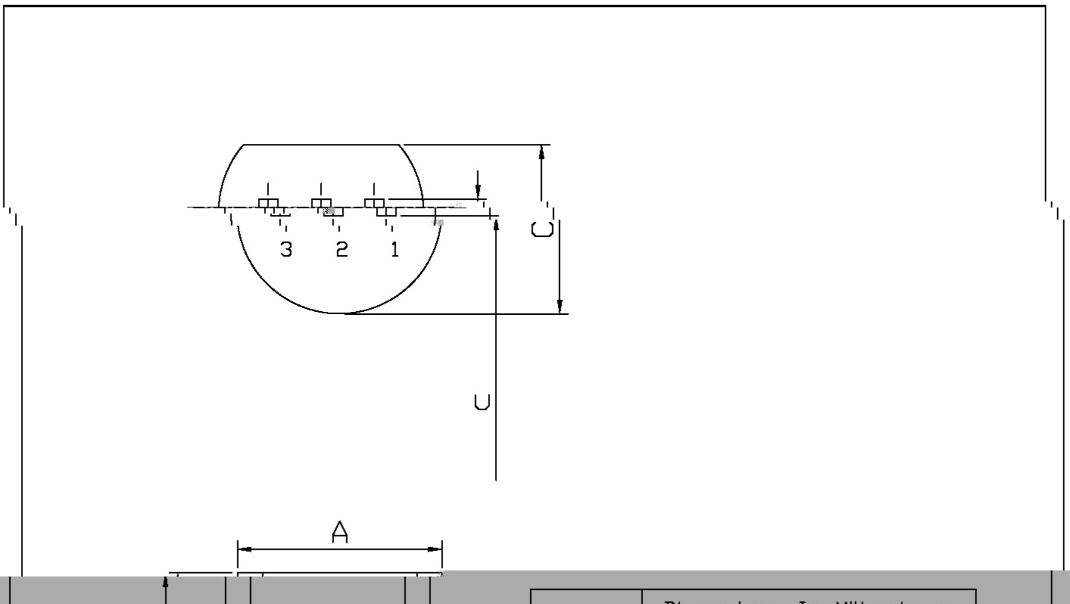




/ Package Dimensions

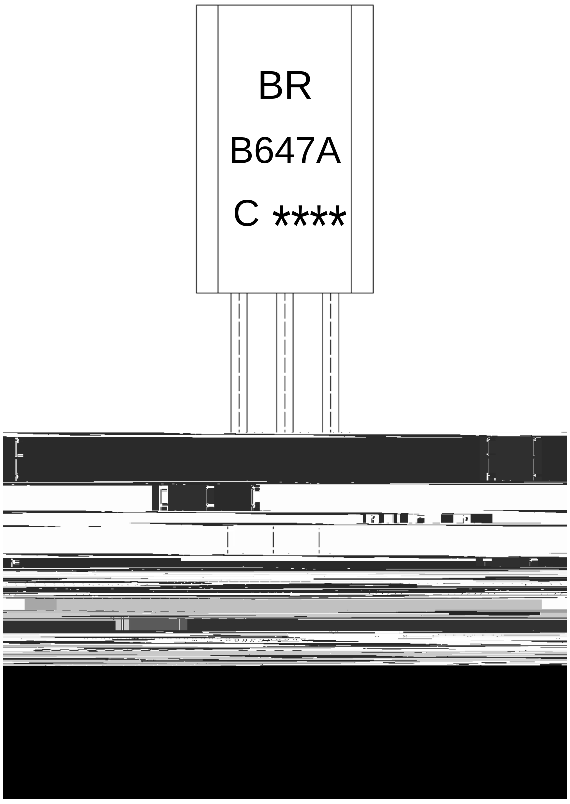
TO-92LM

Unit: mm



Symbol	Dimensions In Millmeters	
	Min	Max
A	4.7	5.1
[illegible]	[illegible]	[illegible]
[illegible]	[illegible]	[illegible]
[illegible]	[illegible]	[illegible]
[illegible]	[illegible]	[illegible]

/ Marking Instructions



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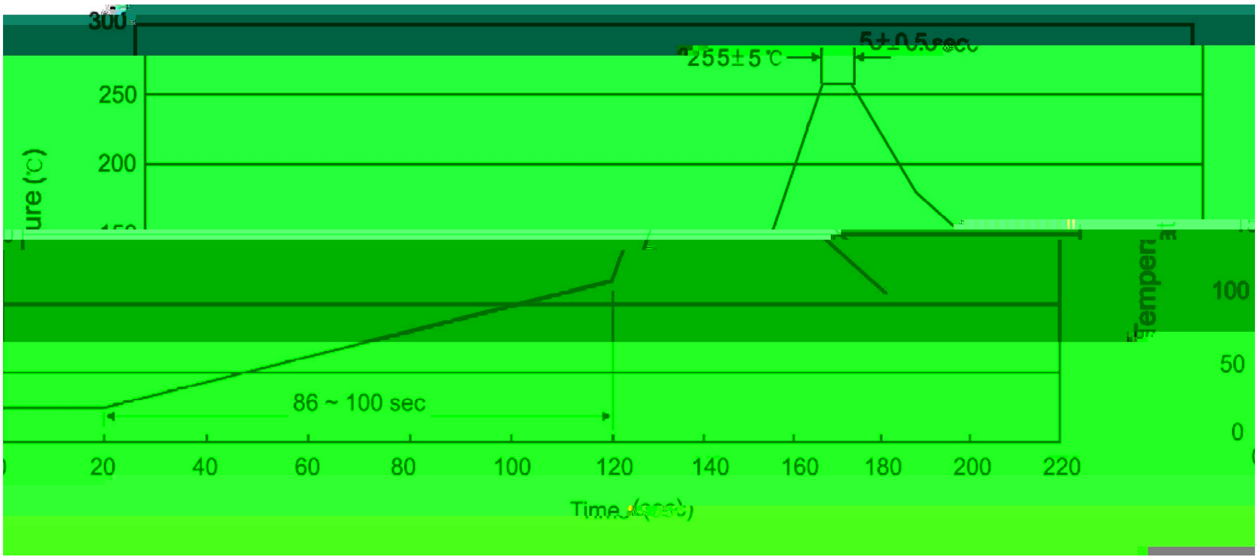
EU:	㎴ ३ ᴻ Ꮒ
E97 : D χ	㎴ ʒ ᴺ Ꮒ
F:	㎴ k ɪʜ ˂ ⁶ Ꮒ
---- χ	㎴ ɫ ғ ῄ ᴺ Ꮒ ɪ ɕ ɫ ғ ῄ ᴺ ψ ק
Note:	
BR:	Company Code.
B647A:	Product Type.
C:	h_{FE} Classifications Symbol
****:	Lot No. Code,code change with Lot No.

(   ) / Temperature Profile for Dip Soldering(Pb-Free)



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1   ĺ ♬ □ ẙ 25 Ḱ 150 ɫ ı ˋH ڗ 60 Ḱ 90sec;

Note:

1.Preheating:25~150 ɫ