

一、 描述

SOT-89 PNP Silicon PNP transistor in a SOT-89 Plastic Package.

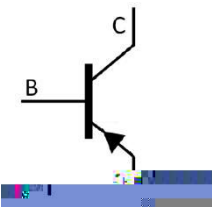
二、 特性

1. 符合 JEDEC 标准， 2SD1766
2. 低 $V_{CE(sat)}$ ， complements the 2SD1766.

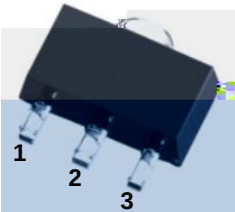
三、 应用

Medium power amplifier applications.

四、 等效电路



五、 引脚



PIN1 Base PIN 2 Collector PIN 3 Emitter

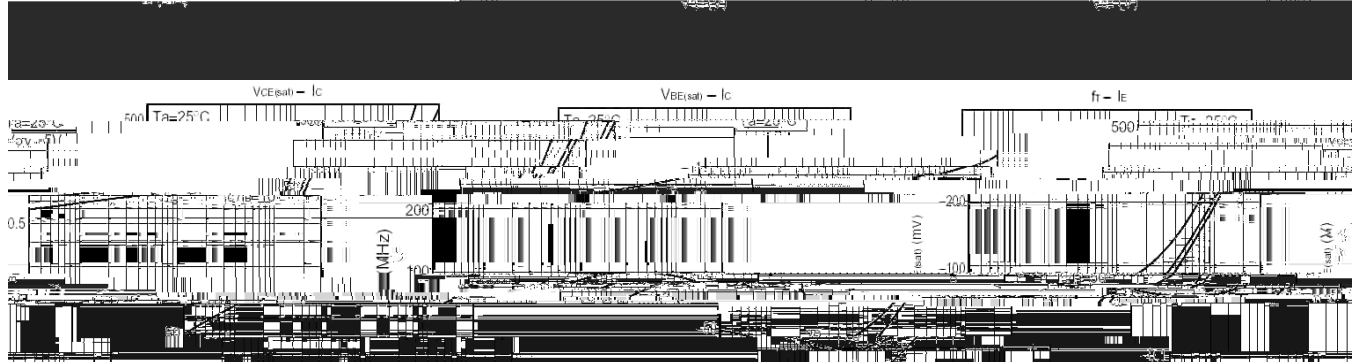
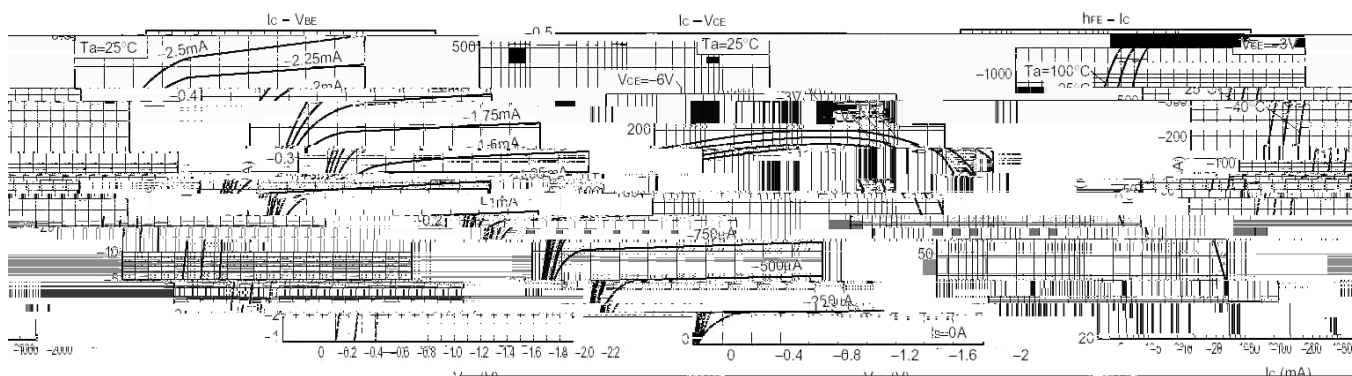
六、 标记

h_{FE} Classifications Symbol	P	Q	R
h_{FE} Range	82 180	120 270	180 390
Marking	HBCP *	HBCQ *	HBCR *

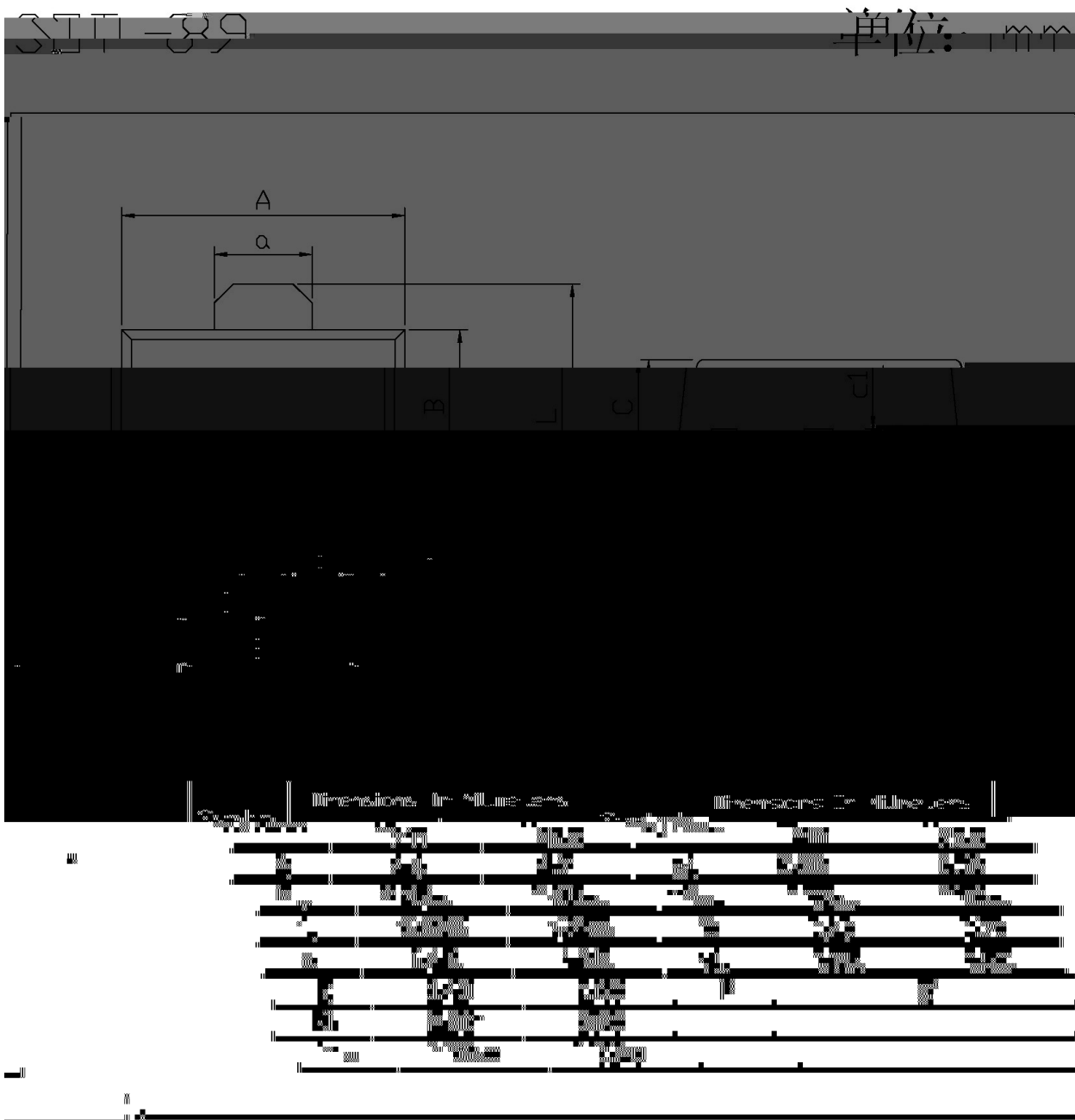
Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-40	V

Colle

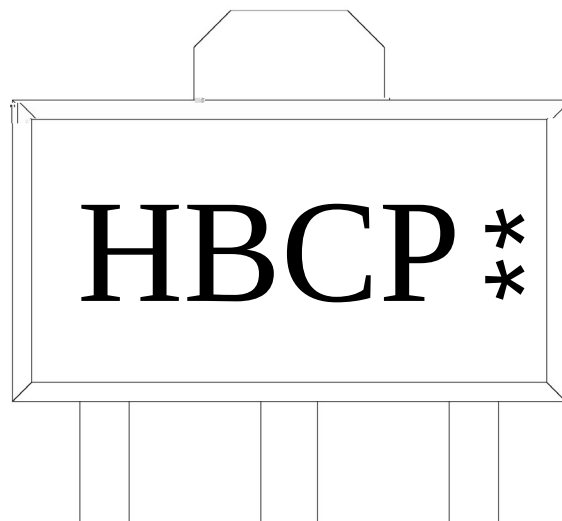
电 压 电 流 特 性 曲 线 / Electrical Characteristic Curve



⌘ È Ò Ì f / Package Dimensions



1. 标记 / Marking Instructions



H

BC

P h_{FE}

**

Note:

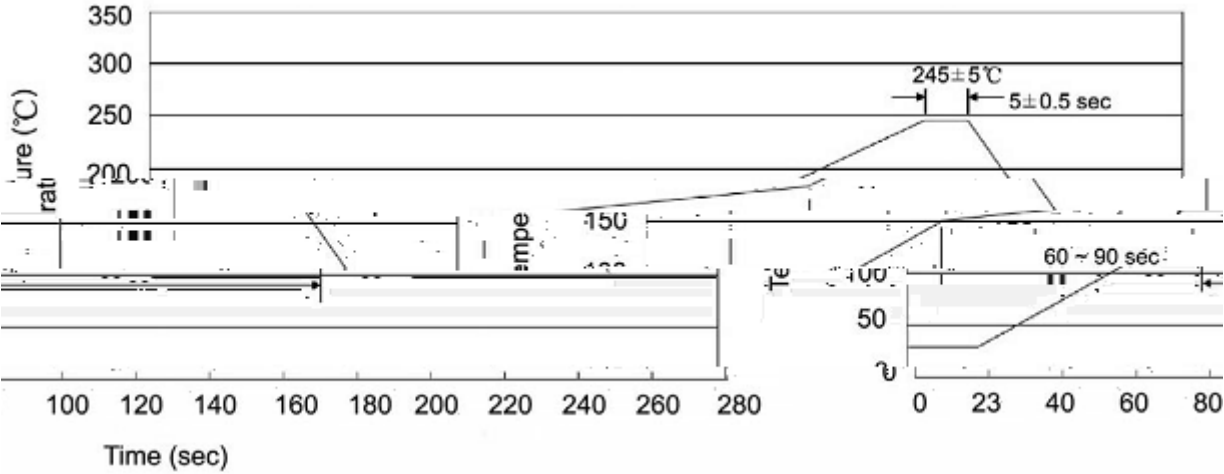
H: Company Code.

BC: Product Type.

P h_{FE} Classifications Symbol

**: Lot No. Code, code change with Lot No.

温度特性 / Whp shudwxuh Suriloh i ru LU Uhior z Vroghulq j+Se0l uhh,



Note:

- | | | | | | |
|---|-------|-----|----|-----------|---|
| 1 | 25 | 150 | 60 | 90sec; | 1.Preheating:25~150 , Time:60~90sec. |
| 2 | 245±5 | | | 5±0.5sec; | 2.Peak Temp.:245±5 , Duration:5±0.5sec. |
| 3 | | | 2 | 10 /sec. | 3. Cooling Speed: 2~10 /sec. |

电阻焊 / Resistance to Soldering Heat Test Conditions

260±5 10±1 sec. Temp.:260±5 Time:10±1 sec

包装 / Packaging SPEC.

/ REEL

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm ³)		
	Units/Reel 只/卷盘	Reels/Inner Box 卷盘/盒	Units/Inner Box 只/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Reel	Inner Box 盒	Outer Box 箱
SOT-89	1,000	7	7,000	8	56,000	7 ×12	180×120×180	385×257×392

注意事项 / Notices